

FIG. 1

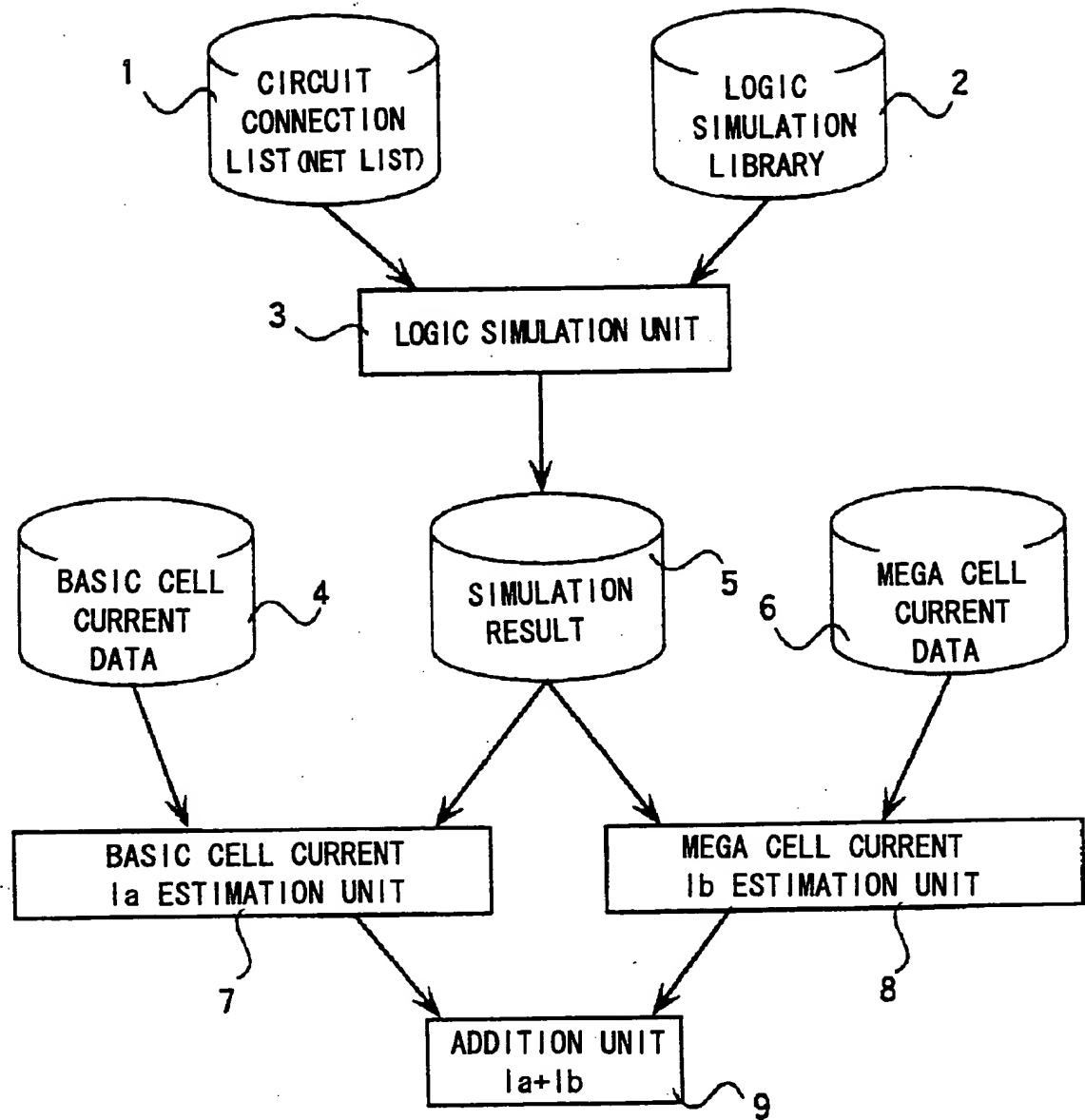


FIG. 2

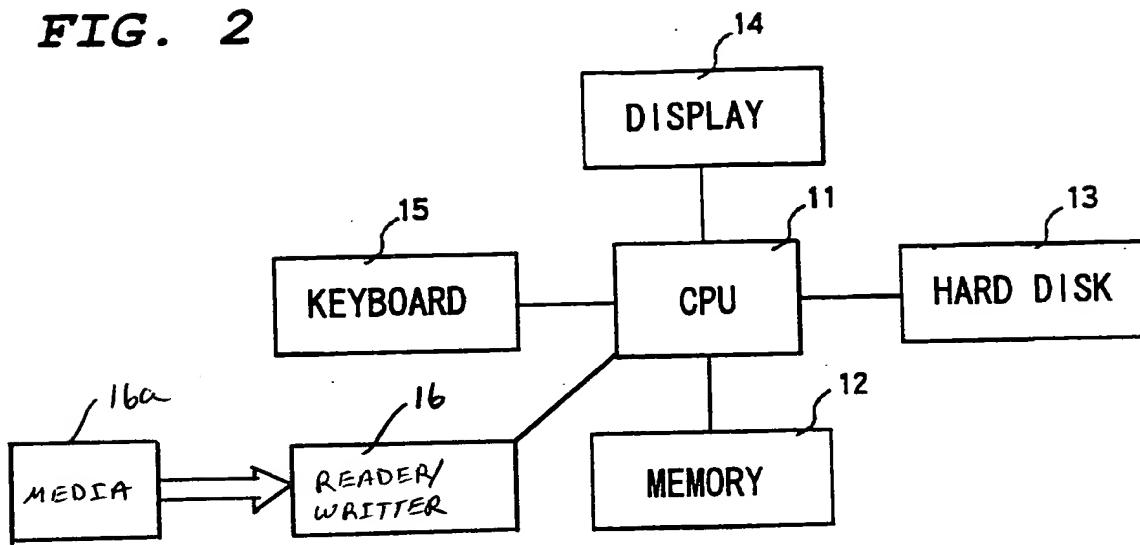


FIG. 3

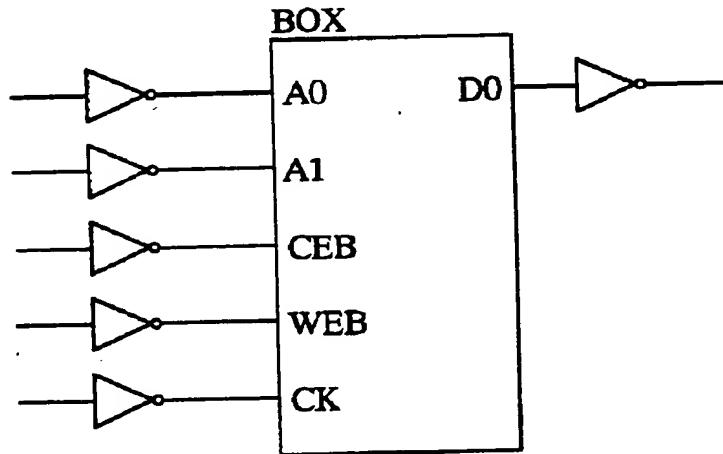


FIG. 4

CELL NAME	RAM
CELL TYPE	SYNCHRONOUS
FREQUENCY SIGNAL	CK
STATE LIST	standby, read, write

FIG. 5

STATE	STATE DEFINING CONDITION	AC CURRENT	DC CURRENT
standby	CEB=1	0	S
read	WEB=1 && CEB=0	$X \times f$	D0-Z(1)
write	WEB=0 && CEB=0	$Y \times f$	0

FIG. 6

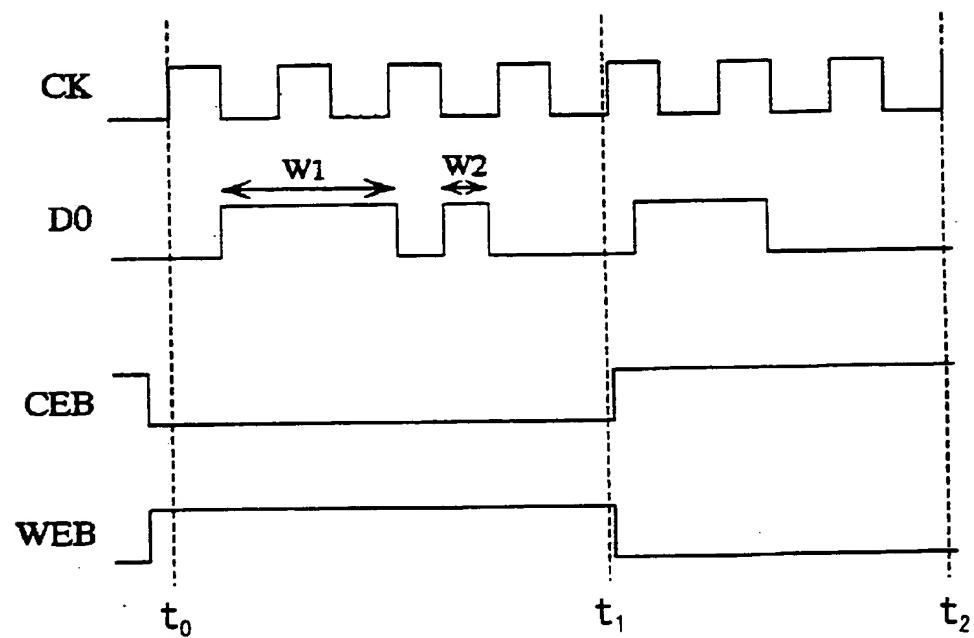


FIG. 7

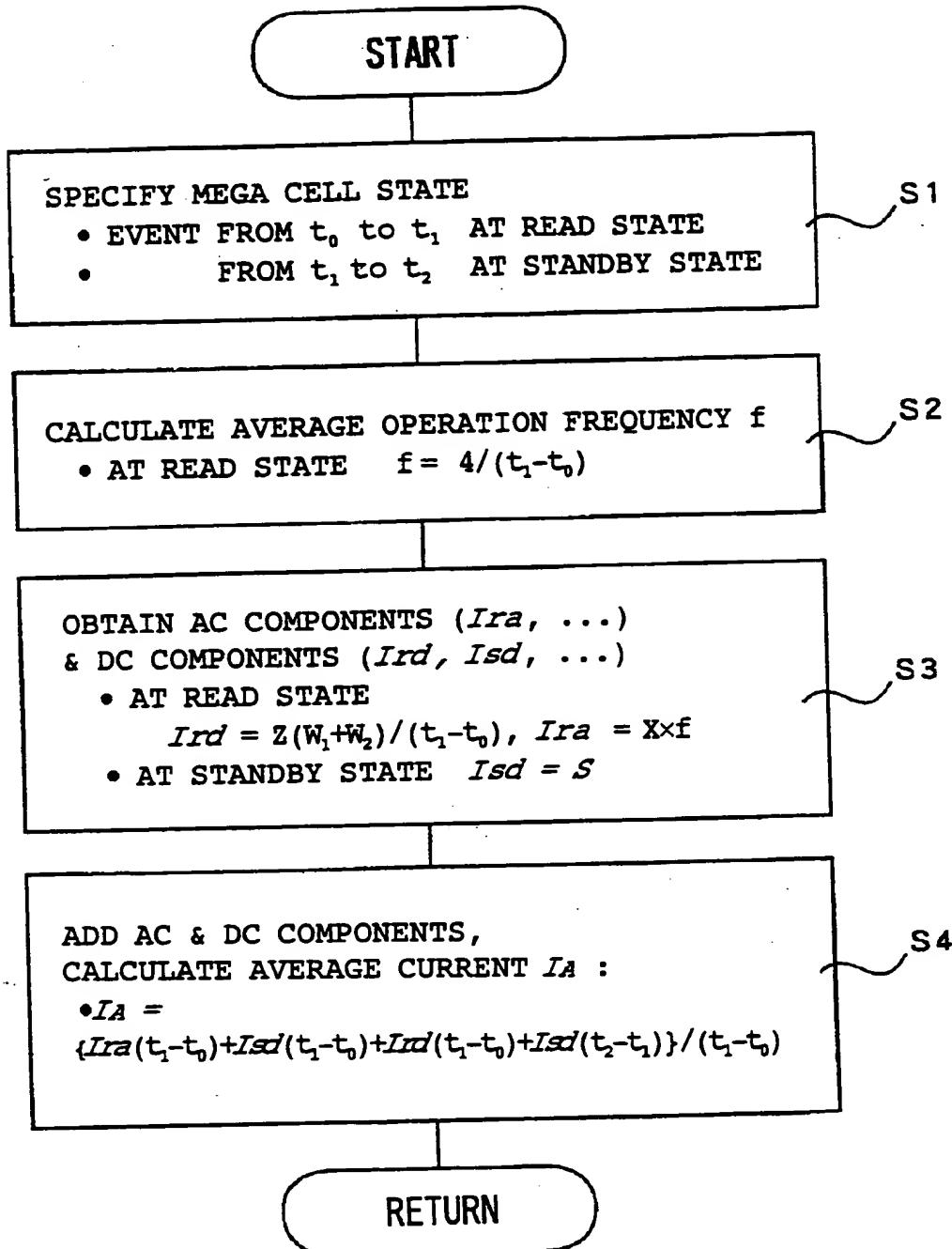


FIG. 8

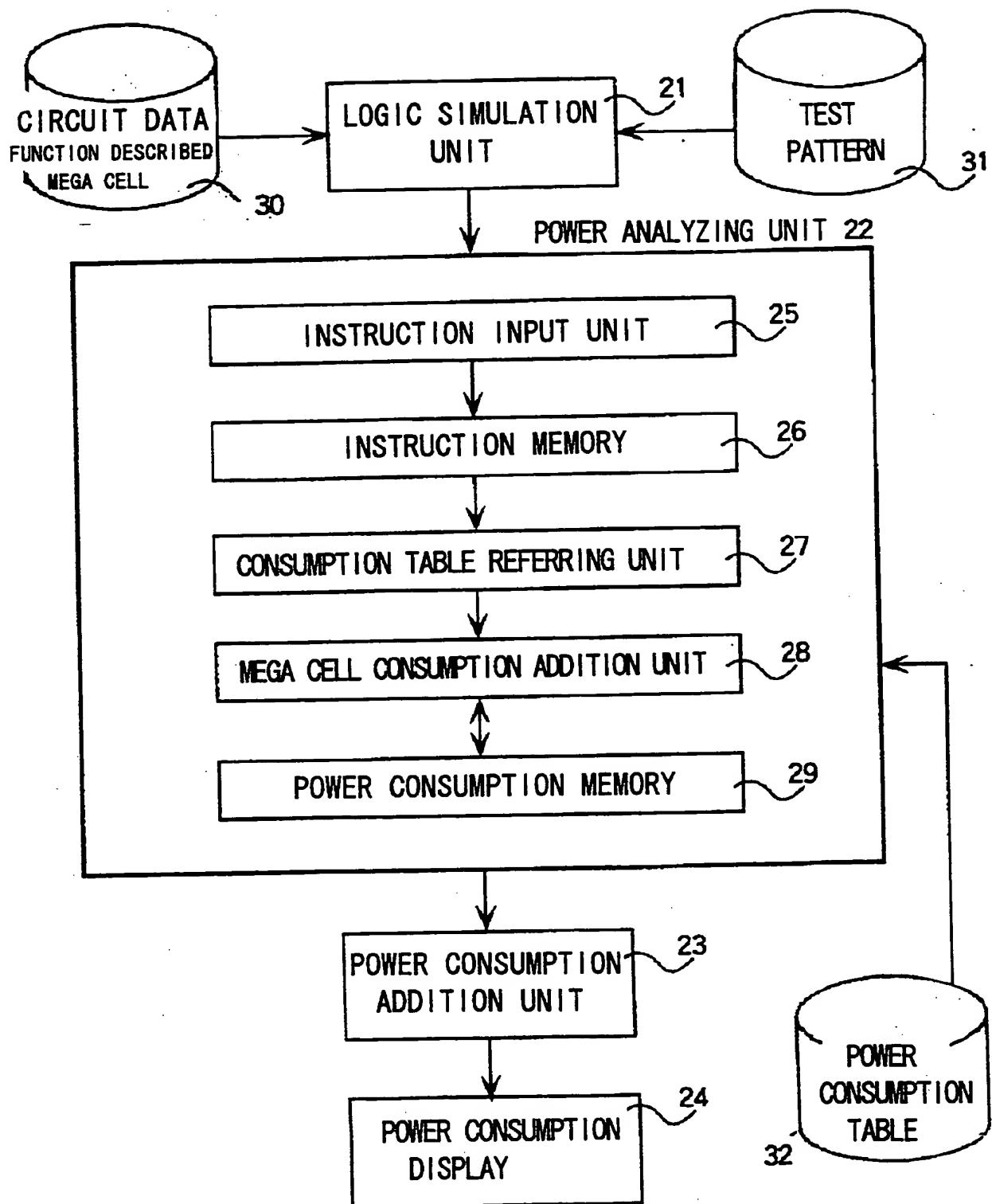


FIG. 9

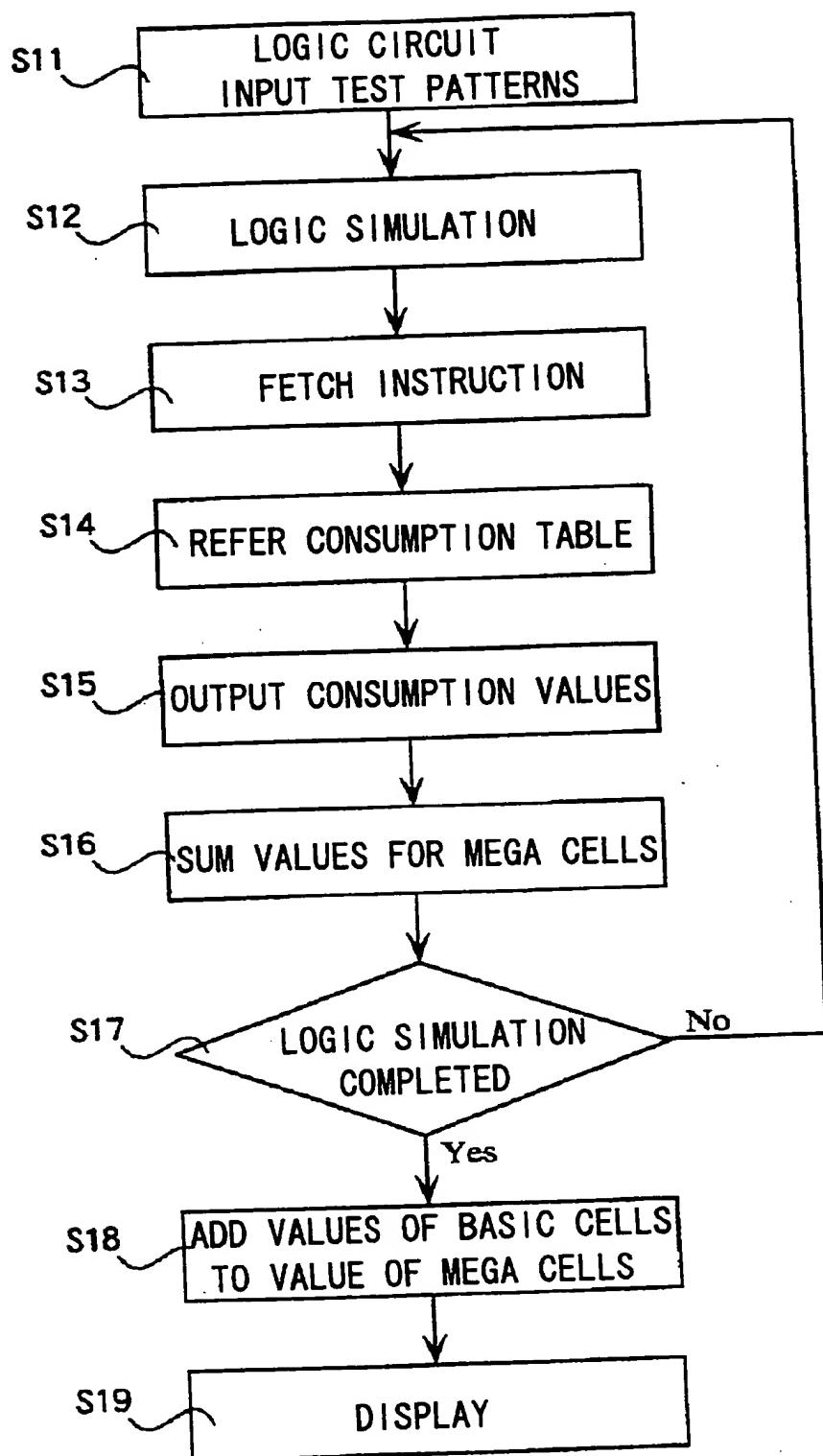


FIG. 1

INSTRUCTION	POWER CONSUMPTION
ADD	W _O
LDA	W _a
STA	W _b

FIG. 11

```
•  
always begin  
•  
    Inst_LDA;  
•  
    Inst_STA;  
•  
end  
•  
task Inst_LDA;  
begin  
    $PowerAnalysis("LDA");  
end  
endtask  
•  
task Inst_STA;  
begin  
    •  
    $PowerAnalysis("STA");  
end  
endtask  
•
```